TRANSMITTAL

Electronic Version v1.1
Stylesheet Version v1.1.0

Title of Invention

HIGH TOLERANCE TCR BALANCED HIGH CURRENT RESISTOR FOR RF CMOS AND RF SiGe BICMOS APPLICATIONS AND CADENCED BASED HIERARCHICAL PARAMETERIZED CELL DESIGN KIT WITH TUNABLE TCR AND ESD RESISTOR BALLASTING FEATURE

Application Number:

Date:

First Named Applicant:

Ebenezer E. Eshun

Confirmation Number:

Attorney Docket Number: BUR920030058US1

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Documents being submitted:	Files
us-ids	BUR920030058US1-usidst.xml
	us-ids.dtd
	us-ids.xsl
us-assignment	BUR920030058US1-usassn.xml
	us-assignment.xsl
	us-assignment.dtd
	BUR920030058US1Assign1.tif
	BUR920030058US1Assign2.tif
us-request	BUR920030058US1-usrequ.xml
	us-request.dtd
	us-request.xsl
us-fee-sheet	BUR920030058US1-usfees.xml
	us-fee-sheet.xsl
	us-fee-sheet.dtd
application-body	BUR920030058US1spec-trans.xml
	us-application-body.xsl
	application-body.dtd
	wipo.ent
	mathml2.dtd
	mathml2-qname-1.mod
	isoamsa.ent
	isoamsb.ent
	isoamsc.ent
	isoamsn.ent
	isoamso.ent
	isoamsr.ent
	isogrk3.ent
	isomfrk.ent
	isomopf.ent
	isomscr.ent
	isotech.ent
	isobox.ent
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	isocyr2.ent
	isodia.ent
	isolat1.ent
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	soextblx.dtd
	BUR920030058US1FIGSsheet1of11.tif
	BUR920030058US1FIGSsheet2of11.tif
	BUR920030058US1FIGSsheet3of11.tif
	BUR920030058US1FIGSsheet4of11.tif
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us-declaration	BUR920030058US1Dec2.tif
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	BUR920030058US1FIGSsheet8of11.tif

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